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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

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Chen, et al.

Docket No.:

TSM03-0586

Serial No.:

10/723,771

Art Unit:

2812

Filed:

11/26/2003

Examiner:

Lindsay Jr., Walter Lee

For:

Method of Fabricating High Voltage Transistor

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Respectfully submitted,

Judy A. Betts Legal Assistant

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Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Election

Dear Sir:

In response to Examiner's Restriction Requirement mailed January 28, 2005 Applicants respectfully elect Group I, claims 1-18, without traverse.

Please contact Applicants' attorney with any questions regarding this matter.

Respectfully submitted,

Steven H. Stater

Reg. No. 35,361

Attorney for Applicants

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